REMARKS

Claims 1, 3-4, 6, 8-11, 13-16, 18-20, 22, 24-27, and 29-30 stand rejected under 35 U.S.C.

§ 103(a) as being unpatentable over United States Patent Number 6,345,349 to Coulson

(hereinafter Coulson) in view of United States Patent Application 2004/0117606 by Wang et al.

(hereinafter Wang) in further view of United States Patent Number 5,590,337 to Rahman et al.

(hereinafter Rahman) and in further view of United States Patent Number 7,047,475 to Sharma et

al. (hereinafter Sharma). Claims 5 and 21 are rejected under 35 U.S.C. 103(a) as being

unpatentable over Coulson in view of Wang, Rahman, and Sharma, and in view of United States

Patent Number 6,678,858 to Imazato (hereinafter Imazato). Claims 31-36 stand rejected under

35 U.S.C. § 103(a) as being unpatentable over Coulson in view of Wang in view of Rahman and

Sharma and in further view of United States Patent Publication 2002/0157054 by Shin et al.

(hereinafter Shin).

Applicants thank the Examiner for the telephone interview of May 25, 2007. We

discussed a proposed amendment, and potential differences with the prior art. Applicants agreed

to submit the amendment for examination.

Amendments to claims.

Applicants have amended claims 1, 6, 11, 16, 22 and 27 with the limitation "…correct

single bit errors in the prefetched data packet…" Claim 1 as amended. The amendment is fully

supported by the specification. Page 14, ¶ 50. In addition, Applicants have amended claims 1, 6,

11, 16, 22 and 27 with the limitations of claims 5, 21, and 31-36 of signaling "…an interrupt to

initiate the error recovery process in response to the set flag, wherein the error recovery process comprises re-retrieving the data packet from the first location..." Claim 1 as amended. The amendment is fully supported by the specification. Page 14, ¶ 54; pages 15-16, ¶ 57; pages 18-19, ¶ 70.

Claims 1, 6, 11, 16, 22 and 27 are further amended with the limitation of storing "...a first location address for the prefetched data packet with the uncorrectable error, wherein the address is stored in the second location and no address is stored for prefetched data packets without uncorrectable errors..." Claim 1 as amended. The amendment is well supported by the specification, which shows one address stored for a prefetched data packet with uncorrectable errors and teaches storing the address. Fig. 4, ref. 410; page 17, ¶ 63. Applicants have canceled claims 3-5, 8-10, 13-15, 18-21, 24-26, and 29-36.

Response to rejections of claims under 35 U.S.C. § 103(a)

Claims 1, 3-4, 6, 8-11, 13-16, 18-20, 22, 24-27, and 29-30 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Coulson in view of Wang, Rahman, and Sharma. Claims 5 and 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Coulson in view of Wang, Rahman, Sharma, and Imazato. Claims 31-36 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Coulson in view of Wang in view of Rahman and Sharma and in further view of Shin. Applicants have canceled claims 3-5, 8, 10, 18, 19, 20, 21, 24-26, and 29-36. Applicants respectfully traverse the rejections of claims 1, 6, 11, 16, 22, and 27.

Claims 1, 6, 11, 16, 22, and 27 as amended include the limitation "...store a first location

address for the prefetched data packet with the uncorrectable error, wherein the address is stored in the second location and no address is stored for prefetched data packets without uncorrectable errors…" Claim 1 as amended. See also claims 6, 11, 16, 22, and 27 as amended. Thus the address for a prefetched data packet with an uncorrectable error is stored, while the addresses for prefetched data packets without uncorrectable errors are not stored. See Fig. 4, ref. 410; page 17, ¶ 63.

In contrast, Coulson, Wang, Rahman, Sharma, Imazato, and Shin do not teach storing an address for a prefetched data packet with an uncorrectable error, while the addresses for prefetched data packets without uncorrectable errors are not stored. Coulson teaches storing mass storage addresses (first location addresses) with **all** main memory addresses (second memory locations), but does not teach that no address is stored for prefetched data packets without uncorrectable errors. Because Coulson, Wang, Rahman, Sharma, Imazato, and Shin do not disclose all of the elements of the present invention, Applicants submit that claims 1, 6, 11, 16, 22, and 27 are allowable.

As a result of the proceeding remarks, Applicants submit that the application is in condition for allowance. If any impediments to the prompt allowance of the claims can be resolved by a telephone conversation, the Examiner is respectfully requested to contact the undersigned.

Respectfully submitted,

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